

Computer-Aided Design of Semiconductor Mounts in Fin-Line Technology

H. El Hennawy and K. Schunemann. "Computer-Aided Design of Semiconductor Mounts in Fin-Line Technology." 1981 MTT-S International Microwave Symposium Digest 81.1 (1981 [MWSYM]): 307-309.

Based on both a rigorous eigenmode calculation and a modal analysis of discontinuities, a computer-aided design of some diode mounts in fin-line technology has been performed. Theoretical results on a detector with Schottky-diode, a reflection-type phase modulator with pin-diode, a switch with two pin-diodes, and a Gunn oscillator show good agreement with measurements. Finally, some design rules are outlined for mounting semi-conductor devices in a fin-line.

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